

INITIAL/LOGO		ABBREVIATIONS	INITIAL/LOGO	ABBREVIATIONS
	CRA	Pre Aperature Gain Control	DISCS	Dis Chip Select
	CRST	Camera Reset	DISP	Display
	CS	Chip Select	DL	Delay Line
	CS 0-7	Chrominance Signal Out 0-7	DOBCK	Audio A/D Convertor Bit Clock
	CSEL	Clock Phase Select	DOCTL	Data Output Control Signal
	CSI 0-7	Chrominance Signal In 0-7	DODAT	Serial Data (to D/A Converter)
	CTSW	Crosstalk Switch	DOLRCK	Audio A/D Converter LR Clock
	CURR	Current	DOLRCK	L/R Clock (to D/A Converter)
	CW	Clockwise	DOMCK	Audio A/D Converter Master Clock
	CYL EC	Cylinder Motor Trque Control	DOMCK	Master Clock (to D/A Converter)
	CYL PG	Cylinder Motor PG	DQ 1-16	Memory Data
	CYL VM	Cylinder Motor Current or Power	DRAM CAS	D-RAM Colum Address Strobe
			DRAM OE	D-RAM Out Enable
D	D CLK	Digital Clock	DRAM RAS	D-RAM Read Address Strobe
	D MODE	Digital Mode Switch Signal	DREC	AV Delayed REC Start Pulse
	D01-03	Zoom 01-03	DRK	Dark (LPF Switch for Auto Focus)
	DA UV SEL	D/A Convertor U/V Select	DS1, 2	Double Sampling Pulse
	DAC	Digital Analogue Converter	DSF 0-7	Data In/Out for Shaffling Memory
	DAG	Digital Analogue Ground	DSF 0-7	Input/Output Data to Shuffling Memory (18MHz)
	DB0-7	Data 0-7	DSP	Digital Signal Processor
	DB0-7	Microprocessor Data	DSP R/B	DSP IC Rady/Busy
	DCC	DC Clamp Control	DSP-48K-H	DSP IC Clock Select
	DCCNT	DC Control	DSTB	Data Strobe Signal
	DCI	Digital Channel Cording IC	DSV	Digital Sum Variation
	DCLR	Digital Clear	DV	Digital Video
	DCP	Digital Clamp Pulse	DVB	Digital Video Broadcast
	DCS-CLK, DA	CAS & DV I/F Serial Clock	DVC	Digital Video Cassette
	DC-STP1	DCS Serial Start	DVDD	Digital VDD
	DC-STP2	DCS Serial Stop	DVIO	Digital Video Input Output
	DCT	Discrete Cosine Transform (Compression)	DVSS	Digital Ground
	DCX7	Serial Data	DX	Shift Data for X Direction (for LCD)
	DEDP 0-3	Playback Data	DY	Shift Data for Y Direction (for LCD)
	DEDR 0-3	Rec Data	DY	TFT Y-axis Shift Data
	DEMO	Demodulation	DZ	Digital Zoom
	DEMP	A/D Convertor Empahsis Control		
	DEMP	De-Emphasis	E E Snap	Electric Snap Shot
	DFD 0-7	Encode Data In/Out Between Shaffling Memory	E ZM	Electric Zoom
	DFD0-7	Encode Input/Output Signal for Shuffling Memory	E2 CS or E2P CS	EEPROM Chip Select
	DIBDCK	Bit Clock	E2 R/B	EEPROM Rady/Busy
	DICLK	Digital Clock	E2P	EEPROM
	DIDAT	Serial Data	EARP	Earphone
	DIDAT	Serial Data Durring Digital Audio In	EC	Torque Control
	DIF	Digital Interface	ECC	Error Correction Cording
	DILRCK	L/R Clock	ECM	Electric Condencer Mic
	DILRCK	Serial Clock Durring Digital Audio In	ECR	Reference Voltage for Capstan Torque
	DIMCK	Master Clock	EDA	Error Correction, DCI, ATF Servo
	DIMCK	Mater Clock Durring Digital Audio In	EE CS	EEPROM Chip Select
	DIO 1-8	Data In/Out	EE R/B	EEPROM Read (H)/Busy (L)
	DIOS	Data In/Out Select Control Signal	EEPROM	Electric Erasable Programable Read Only Memory
	DIOS	Select Signal for Digital In/Out	EIS	Electric Image Stabilizer (DIS)
	DIS	Digital Image Stabilizer	EMP	A/D Convertor Emphasis Control
	DIS R/B	Digital Image Stabilizer Read (H)/Busy (L)	ENAB	Enable
	DIS R/B	DIS IC Rady/Busy	ENV	Enverope
	DIS/KAND	Digital Image Stabilizer/Sensitivity	EOB	End of Block